

## WEST Search History





DATE: Sunday, February 06, 2005

Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
		<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L32	L31 and @pd > 20040603	0
<input type="checkbox"/>	L31	('4918379')!.PN.	2
<input type="checkbox"/>	L30	('6101457')!.PN.	2
<input type="checkbox"/>	L29	L28 not L26	6
<input type="checkbox"/>	L28	ram near(block or plural\$4 or group or second) same (interfac\$3 or module or parallel)same (lead or pad\$4 or pin or terminal or port or inter\$1port or bus or interfac\$4 or 'i/o' or 'io' or input or output or access\$3 or inter\$1connec\$5 or net) near test	20
<input type="checkbox"/>	L27	('6128758')!.PN.	2
<input type="checkbox"/>	L26	ram near(block or plural\$4 or group or second) same (module or parallel)same (lead or pad\$4 or pin or terminal or port or inter\$1port or bus or interfac\$4 or 'i/o' or 'io' or input or output or access\$3 or inter\$1connec\$5 or net) near test	14
<input type="checkbox"/>	L25	L20 and test bus	2
<input type="checkbox"/>	L24	L21 and test with bus	4
<input type="checkbox"/>	L23	('US20030009712A')!.PN.	0
<input type="checkbox"/>	L22	('20030009712')!.PN.	2
<input type="checkbox"/>	L21	ram block same module same test\$3 and L20	13
<input type="checkbox"/>	L20	ram block same module	101
<input type="checkbox"/>	L19	L18 and (dedicat\$3 or special)near bus same test\$3 and (tap or jtag) and ram	6
<input type="checkbox"/>	L18	L16 and (dedicat\$3 or special)near bus same test\$3 and (tap or jtag)	18
<input type="checkbox"/>	L17	(dedicat\$3 or special)near bus same test\$3 same ram	1
<input type="checkbox"/>	L16	(dedicat\$3 or special)near bus same test\$3	118
<input type="checkbox"/>	L15	('5423050')!.PN.	2
<input type="checkbox"/>	L14	L13 and jtag and test bus with (ram or rom or memory)same compar\$4 same (switch\$3 or select\$3 or multiplex\$3 or mux or de\$1multiplex\$3 or de\$1mux)	1
<input type="checkbox"/>	L13	L12 and compar\$4 and (switch\$3 or select\$3 or multiplex\$3 or mux or de\$1multiplex\$3 or de\$1mux)	168
<input type="checkbox"/>	L12	L11 and jtag and (ram or rom or memory)	192
<input type="checkbox"/>	L11	test bus	1451
<input type="checkbox"/>	L10	L9 and @pd > 20050126	0
<input type="checkbox"/>	L9	('20030167427'  '6587979'  '6665817')!.PN.	6

L5 and 'bist' and 'jtag' and control\$4 same (TEST\$3 or dedicat\$3) with BUS

<input type="checkbox"/>	L8	with EMBED\$4 same (memor\$7 or rom or non\$1volatil\$2 or read\$1only or ram or random or eeprom or flash) same system	3
<input type="checkbox"/>	L7	('6587979')!.PN.	2
<input type="checkbox"/>	L6	('6665817')!.PN.	2
<input type="checkbox"/>	L5	L4 and 'jtag' and control\$4 same (TEST\$3 or dedicat\$3) with BUS with EMBED\$4 same (memor\$7 or rom or non\$1volatil\$2 or read\$1only or ram or random or eeprom or flash) same system	8
<input type="checkbox"/>	L4	L3 and control\$4 same (TEST\$3 or dedicat\$3) with BUS with EMBED\$4 same (memor\$7 or rom or non\$1volatil\$2 or read\$1only or ram or random or eeprom or flash) same system	23
<input type="checkbox"/>	L3	L2 and (TEST\$3 or dedicat\$3) with BUS with EMBED\$4 same (memor\$7 or rom or non\$1volatil\$2 or read\$1only or ram or random or eeprom or flash) same system	42
<input type="checkbox"/>	L2	L1 and (TEST\$3 or dedicat\$3) with BUS with EMBED\$4 same (memor\$7 or rom or non\$1volatil\$2 or read\$1only or ram or random or eeprom or flash)	81
<input type="checkbox"/>	L1	(TEST\$3 or dedicat\$3) with BUS with EMBED\$4	190

END OF SEARCH HISTORY